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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Applicant:

Sunil Mehta and Fabiano Fontana

Filing Date:

November 14, 2001

Group:

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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number								Date	Name	Class	Subclass	Filing Date if Appropriate
HN	1	9	2	9	2	9	9	9	05/09/89	Ashmore, Jr.	307	469	04/20/88
HN	8	8	8	2	0	8	8	8	08/29/89	Ashmore, Jr.	307	469	04/20/88
HN	8	8	8	8	8	0	7	7	09/12/89	Ashmore, Jr.	307	469	04/20/88
HN	8	8	8	8	2	1	8	8	12/05/89	Brahmbhatt	365	181	08/19/87
HN	4	9	2	4	2	7	8	8	05/08/90	Logie	357	23.5	11/15/88
HN	5	2	7	0	5	8	7	7	12/14/93	Zagar	307	469	01/06/92
HN	6	0	2	8	7	8	9	9	02/22/00	Mehta et al.	365	185.14	06/15/99

FOREIGN PATENT DOCUMENTS

Document Number								Date	Country	Class	Subclass	Translation	
												Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

HN			Patent Application Serial No. 09/704,487 filed November 2, 2000, with title "Wide Input Programmable Logic System and Method" to Ravindar M. Lall, <u>now a US Patent No. 6,507,212</u>
EXAMINER	HIEN NGUYEN		DATE CONSIDERED 4/16/03

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